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Attorney Docket No.: FUJI:203

Date: January 23, 2002

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The state of the s		By:	Marc A. Rossi	(n·						
ASSIS' Washir		COMMISS D.C. 2023	SIONER FOR PATE	ENTS						
Siri										
Transmitted herewith for filing is the patent application of										
Invento	or(s):	Akio KIT	AMURA of Nagano	, Japan						
For:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND MANUFACTURE METHOD THEREFORE									
Enclose	ed are:									
[X]	FOURTEEN (14) sheets of formal drawings, Figures 1-14.									
[]	An Assignment of the invention to <u>Fuji Electric Co., Ltd.</u> .									
[X]	The application claims priority from Japanese Application 2001-015042 filed January 23, 2001.									
[X]	A certified copy of the priority document is enclosed.									
[]	A Dec	laration an	d Power of Attorney							
[]	A veri	fied statem	ent to establish sma	ll entity status under 37 CFR 1.9 and 37 CFR 1.27.						

- [] A Preliminary Amendment is enclosed.
- [X] An Information Disclosure Statement.

	Number	Numb	er	Basic					
	Filed	Extra		Rate Fe		ee \$74	e \$740.00		
Total									
Claims	19 - 20	= 0	x	\$ 18.00	=	\$	0.00		
Independent									
Claims	3 - 3	= 0	X	\$ 84.00	=		0.00		
Multiple Depe	endent Clai								
(if applicable)				\$280.00	=	28	<u>80.00</u>		
				Total		\$1,02	0.00		
Reduction by 1/2 for filing Small Entity 0.0									
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- Our Check No. _____ in the amount of the total filing fee is enclosed. However, if the check is missing or insufficient, the Commissioner is authorized to charge any additional fees which may be required, or credit any overpayment, to our Deposit Account No. 18-2056.
- [] The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to our Deposit Account No. 18-2056: any patent application processing fees under 37 CFR 1.17; and any filing fees under 37 CFR 1.16 for presentation of extra claims.

Marc A. Rossi

Registration No. 31,923